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<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)	Application / Conf. No.	10/693,568 / 1995
	Filing Date	October 24, 2003
	First Named Inventor	Patrick Lysaght
	Examiner Name	Naum B. Levin
	Art Unit	2825
Express Mail Receipt No.	Patent No.	
Total Number of Pages in This Submission	Attorney Docket Number	X-1374 US

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X-1374 US  
10/693,568

PATENT  
Conf. No.: 1995



IN THE UNITED STATES PATENT OFFICE

Applicants: Patrick Lysaght et al.  
Assignee: Xilinx, Inc.  
Title: Method and Apparatus for Power Optimization During An Integrated Circuit Design Process

Serial No.: 10/693,568 Filed: 10-24-2003  
Examiner: Naum B. Levin Art Unit: 2825  
Docket No.: X-1374 US Conf. No.: 1995

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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION AFTER RCE

Dear Sir:

In response to the Office Action mailed from the Patent Office on October 16, 2006, please replace/substitute the following claims as indicated.

**Amendments to the claims** are reflected in the list of claims that begins on page 2 of this paper.

**Remarks** begin on page 7 of this paper.